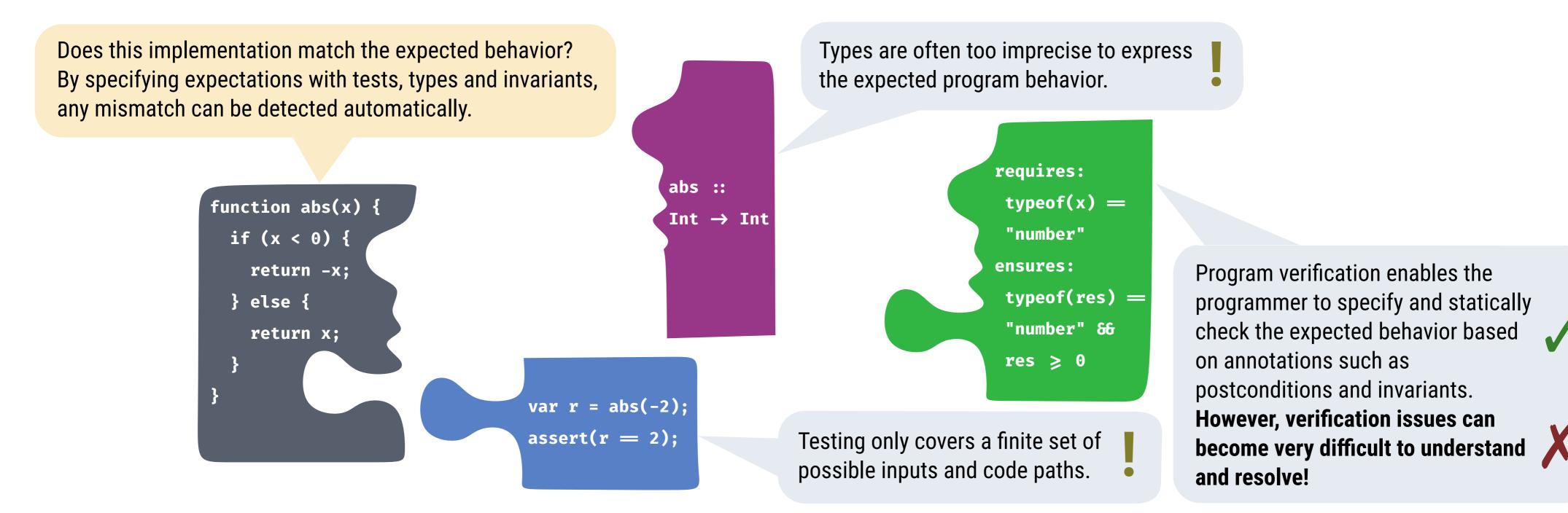
An Integrated Development and Verification Environment for JavaScript

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There are many different ways to check whether a program is correct, such as testing, typechecking and static verification.



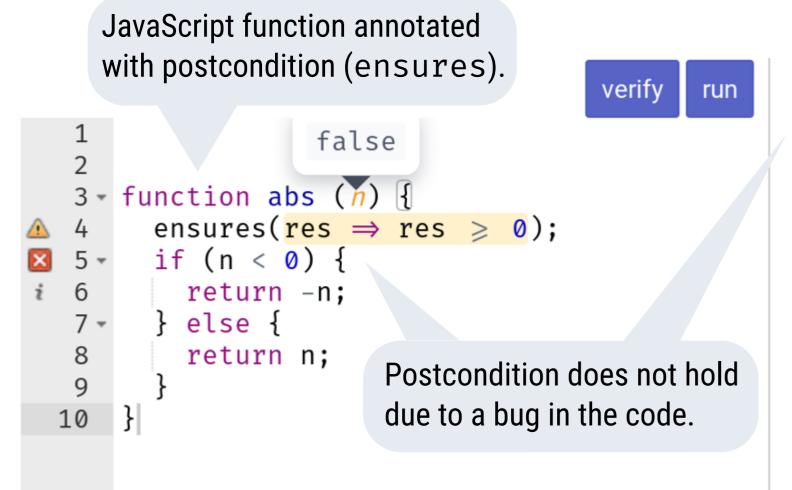
Proposed Solution: Executable Counterexamples and Interactive Verification Tools

Understandable and predictable verification algorithm

- Avoid brittle heuristics and automatic inference

Display concrete counterexamples for free variables

- Use model from SMT solver for failed verification conditions
- Simple values can be shown as popups
- Complex values (such as functions) need to be synthesized



nditions			an interactive, exploratory environment				
naitions nesized			Step-by-step debugging based on generated testcases - Enable traditional debugging experience for verification issues				
run	abs: (1	res ≥ 0)	Verification Inspector displays details.	Try it out yourself! esverify.org/idve			
	ASSUMPTI Assume:	ONS X > 1					
	ASSERTIONS			The programmer can enter additional assumptions and assertions as boolean expressions to resolve this			
nold	<pre>unverified abs: (res ≥ 0)</pre>						
	Assert:			verification issues with interactive experimentation.			
	WATCH EX	PRESSIONS					
	Watch:	х + у					
	SCOPES -						
	n		false	For every verification issue, an			
	abs	abs function abs (n) { .		automatically generated test can be			

Interactive Verification Inspector

- Show details for verification conditions (such as assumptions and assertions)

- Enable programmers to add, remove and manipulate assumptions as part of an interactive, exploratory environment

CALL STACK

<program> (<null>:0:0)

debugged using standard debugging controls such as watch expressions and step-by-step execution.

Evaluation

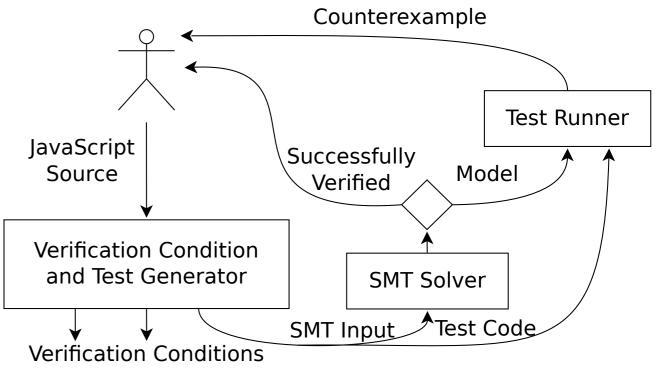
Online user study with 18 participants who were given a tutorial of the integrated development and verification environment followed by a series of small programming and verification tasks and an online survey.

	Response in Survey	Verification Inspector	Counter- examples	Integrated Debugger
\checkmark	Helpful	33%	55%	44%
	UI Issues	55%	39%	44%
	Not useful	6%	6%	6%
X	Impairs Development	11%	0%	6%

Restart Step Into Step Over Step Out

Implementation

Verificaiton conditions are translated to SMT logic. If the SMT solver finds a counterexample, it is used for automatic test generation. Finally, the verification inspector shown above lets users interactively manipulate verification conditions.



Paper at PX'19 workshop. Online Demo: https://esverify.org/idve Source: http://github.com/levjj/esverify-web/ Contact: cschuste@ucsc.edu